

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	47	decod\$3 adj2 (device circuit\$3 component) with "DMA" same (system main) near3 (bus\$3 memor\$3)	US- PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/21 16:58
2	BRS	L2	349	(invalid\$4 null\$3 nullif\$4) adj3 data near4 (eject\$3 image)	US- PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/21 17:01
3	BRS	L3	0	1 and 2	US- PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/21 17:01
4	BRS	L4	62658	"347"/\$.ccls.	US- PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/21 17:01
5	BRS	L5	23	4 and (1 2)	US- PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/21 17:05
6	BRS	L6	375	(main system independen\$2) adj2 bus\$3 same local\$4 adj3 bus\$3 with data near4 transfer\$4	US- PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/21 17:07
7	BRS	L7	1	4 and 6	US- PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/21 17:07